

University of Mumbai



1T01116

Winter 2019

EXAMINATION TIME TABLE T.E.(ELECTRONICS)(Sem VI) (CBSGS)

Days and Dates	Time	Paper Code	Paper
Tuesday, December 03, 2019	02:30 p.m. to 05:30 p.m.	37201	BASIC VLSI DESIGN
Thursday, December 05, 2019	02:30 p.m. to 05:30 p.m.	37202	ADVANCED INSTRUMENTATION SYSTEM
Tuesday, December 10, 2019	02:30 p.m. to 05:30 p.m.	37203	COMPUTER ORGANIZATION
Thursday, December 12, 2019	02:30 p.m. to 05:30 p.m.	37204	POWER ELECTRONICS - I
Monday, December 16, 2019	02:30 p.m. to 05:30 p.m.	37205	DIGITAL SIGNAL PROCESSING AND PROCESSORS
Wednesday, December 18, 2019	02:30 p.m. to 04:30 p.m.	37206	MODERN INFORMATION TECHNOLOGY FOR MANAGEMENT

NOTE: The candidates appearing for the examination should report 20 minutes before the start of examination. Mobile phones and other electronic gadgets are prohibited in the examination hall. Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098
09 Oct, 2019

Sd/-
(Dr. Vinod Patil)
Director
Board of Examinations & Evaluation